ECEN 651 Laboratory Report Format
Modified from the same document for ELEN 248

Reports are due at the beginning of the lab period. The reports are intended to be a complete documentation of the work done in preparation for and during the lab. The report should be complete so that someone else familiar with Verilog and digital design could use it to verify your work.

1. A neat thorough lab report must be presented to your lab instructor at (or before) the beginning of your next lab. Lab reports should be submitted on 8.5” x 11” paper, typed on one side only. Your report is a professional presentation of your work in the lab. Neatness, organization, and completeness will be rewarded. Points will be deducted for any part that is not clear.

2. Each report will contain the following sections:
   a. Cover Page: Include your name, ECEN 651, Lab No., TA’s name, and date.
   b. Objectives: Enumerate the topics that you think the lab will teach you. DO NOT REPEAT the wording in the lab manual procedures. There should be one or two sentences per objective. Remember, you should write about what you will learn, not what you will do. These are not necessarily the same things.
   c. Design: This part contains all the steps required to arrive at your final circuit. This should include diagrams, tables, equations, K-maps, explanations, etc. Be sure to reproduce any tables you completed for the lab. This section should also include a clear written description of your design process. Simply including a circuit schematic or Verilog code is not sufficient. A well commented code and output results are two essential parts of this part.
   d. Schematics: As part of the design process for each lab, you will create a gate-level schematic (when necessary) and turn it in with your report. The schematic must be complete.
   e. Questions: Specific questions asked in the lab should be answered here. Retype the questions presented in the lab and then formally answer them.
3. Late lab report will have 50% of the points deducted for being one day late. If a lab report is 2 days late, a grade of 0 will be assigned.

4. Your work must be original and prepared independently. However, if you need any guidance or have any questions or problems, please do not hesitate to call your Teaching Assistant (TA) during office hours (or send him an email). Copying any lab report will result in a grade of 0. The incident will be formally reported to the University.

5. Each lab exercise (code) must be completed and demonstrated to your TA before the lab schedule for the subsequent lab is due (1 week later) in order to receive working circuit credit. This is the procedure to follow:
   a. Code works: If the code works during the lab period, call your TA, and he/she will sign and date it. You should then save your code on your own work directory before leaving the lab. This is the end of this lab, and you will get a complete grade for this portion of the lab.
   b. Code does not work: If the circuit does not work, you must make use of the open times for the lab room (Zachry 213B) to complete your work. When your circuit is ready, contact your TA to set up a time when the two of you can meet to check your circuit.

6. You are guaranteed a computer and workspace in 213B Zachry only during your lab period. If you need to work on your circuits at a time other than your regularly scheduled lab period it is an open access lab be my guest.

7. Attendance at your regularly scheduled lab period is required. An unexpected absence will result in loss of credit for your lab. Your lab instructor may permit rescheduling if you arrange for the change ahead of time.