ECEN 651 Microprogrammed Control of Digital Systems
Fall 2013

Instructor: Dr. M. Lu
Office: 332E WERC
E-mail: mlu@ece.tamu.edu
Office Hours: T Th 11:30-12:30
Credit: 4 hours
Prerequisite: ECEN350 and 449
Text: J. L. Hennessy and D. A. Patterson
Computer Architecture – A Quantitative Approach
Grading: Homework 25%
Lab 25%
Midterm 25%
Final 25%
Syllabus: Week 1 Ch1 Design Fundamentals
Week 2 Ch1/Notes
Week 3 Notes & Instruction Set
Week 4 Appd. B Instruction Set
Week 5 Appd. B / Appd. A Pipelining
Week 6 Appd. A Review
Week 7 Appd. A / Review
Week 8 Midterm / Ch 2
Week 9 Ch 2 Instruction-level Parallelism
Week 10 Ch2 / Ch3
Week 11 Appd. I Computer Arithmetic
Week 12 Ch5 Memory Hierarchy
Week 13 Appd. C / Review
Week 14 Final